

Applic. No. 10/008,241  
Amdt. dated October 6, 2003  
Reply to Office action of March 31, 2003

Claim Amendments

Claim 1 (currently-amended): An electronic component,  
comprising:

a semiconductor unit having:

a housing having a first side face, and

at least two terminal pins protruding out of said first  
side face of said housing;

said housing having an isolation barrier formed outside said  
housing between said two terminal pins.

Claim 2 (original): The component according to claim 1,  
wherein said isolation barrier is integrally formed onto said  
housing.

Claim 3 (original): The component according to claim 2,  
wherein:

said first side face has a first edge and a second edge  
opposite said first edge; and

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said isolation barrier is a plate extending on said first side face from said first edge to said second edge.

Claim 4 (original): The component according to claim 1, wherein:

said first side face has a first edge and a second edge opposite said first edge; and

said isolation barrier is a plate extending on said first side face from said first edge to said second edge.

Claim 5 (original): The component according to claim 1, wherein:

said isolation barrier has a side remote from said first side face of said housing; and

said two terminal pins protrude beyond said side of said isolation barrier.

Claim 6 (original): The component according to claim 1, wherein said housing has at least one side wall that together with said isolation barrier encloses said two terminal pins from at least three sides.

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Claim 7 (original): The component according to claim 1,  
wherein said at least two terminal pins protrude out of said  
housing next to each other in a row.

Claim 8 (original): The component according to claim 1,  
wherein:

said first side face has a first edge and a second edge  
opposite said first edge;

a first one of said two terminal pins is located closer to  
said first edge than a second one of said two terminal pins;  
and

said second one of said two terminal pins is located closer to  
said second edge than said first one of said two terminal  
pins.

Claim 9 (original): The component according to claim 1,  
comprising:

a plurality of isolation barriers that includes said isolation  
barrier;

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said housing having side walls that together with said plurality of said isolation barriers surrounds at least one of said two terminal pins from four sides.

Claim 10 (cancelled)

Claim 11 (currently-amended): A transistor, comprising:

a semiconductor unit having:

a housing having a first side face; and

at least two terminal pins protruding out of said first side face of said housing;

said housing having an isolation barrier formed outside said housing between said two terminal pins.